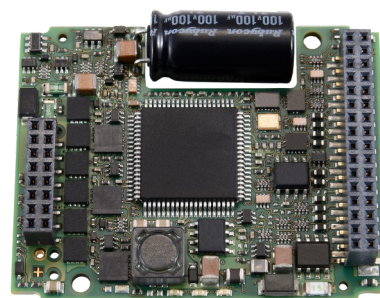


## Servo amplifier

**mcDSA-E62-Modul**

Article number: 1505022



Picture similar

**Technical data**

<b>Power</b>	
Electronic supply voltage Ue	9..30 V
Electronic current consumption @ Ue=24V	typ. 30 mA
Power supply voltage Up	9..60 V
Max. output current	15 A
Continuous output current	5 A
Output voltage	90% Up
PWM frequency	25, 32*, 50 kHz
Min. load inductance	200 µH
<b>Mechanical</b>	
Size LxWxH	52.5 x 41 x 11 mm
Weight	18 g
<b>Environment</b>	
Protection class	IP00
Operating temperature	0..70 °C
Rel. humidity (non-condensing)	5..85 %
<b>Encoder</b>	
Type	sin / cos
Signals	+Sin,-Sin,+Cos,-Cos
Resolution	13 bit per sine period
Input voltage	1 V peak-peak, differential
Signal type	sine/cosine, analog, differential
<b>Digital inputs</b>	
Number	4 (Din0..3)
Low voltage	-30..5 V
High voltage	6..30 V
<b>Digital outputs</b>	
Number	1 (Dout0)
Continuous output current	1.5 A
Load	resistive, induktive
Output voltage	Electronic supply voltage Ue
Signal type	positive switching
<b>Analog inputs</b>	

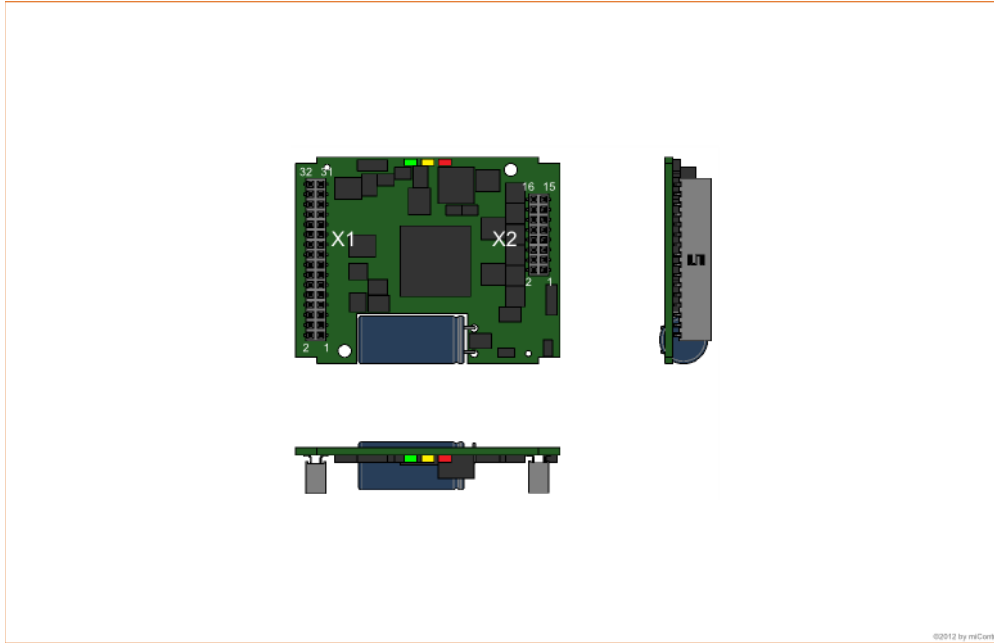
Number	2 (Ain0..1)
Signal type	0..10 V, 12 Bit, single ended
<b>CAN bus</b>	
Protocol	DS301
Device profile	DS402
Max. baudrate	1 Mbit/s
CAN specification	2.0B
Galvanically isolated	no

\* default value

Additional technical data are available in mcManual.



Scheme



Terminal assignment

X1	Hall, inc. encoder, I/O's and CAN	
1	res.	Reserved
2	Id7	Node id bit 7
3	+U5V	5V auxiliary voltage (hall and encoder)
4	Id6	Node id bit 6
5	+Cos	Encoder, plus cosine signal
6	Id5	Node id bit 5
7	+Sin	Encoder, plus sine signal
8	Id4	Node id bit 4
9	res.	Reserved
10	Id3	Node id bit 3
11	-Cos	Encoder, minus cosine signal
12	Id2	Node id bit 2
13	-Sin	Encoder, minus sine signal
14	Id1	Node id bit 1
15	CAN Lo	CAN Low
16	Id0	Node id bit 0
17	CAN Hi	CAN High
18	Erw2	mcSPI expansion signal 2
19	Dout0	Digital output 0
20	Erw1	mcSPI expansion signal 1
21	Din2	Digital input 2
22	SpiSCK	mcSPI Clock
23	Din1	Digital input 1
24	SpiMOSI	mcSPI Master Out Slave In
25	Din0	Digital input 0
26	Spi/SS	mcSPI Slave Select
27	Ain0	Analog input 0
28	SpiMISO	mcSPI Master In Slave Out
29	Ain1	Analog input 1
30	Din3	Digital input 3
31	GND	Ground for 5V auxiliary voltage (hall and encoder)
32	res.	Reserved

X2	Motor	
1	+Up	Power supply voltage
2	res.	Reserved
3	+Up	Power supply voltage
4	res.	Reserved
5	GND	Ground for power and electronic supply voltage
6	GND	Ground for power and electronic supply voltage
7	Ma	Motor phase A
8	+Ue	Electronic supply voltage
9	Ma	Motor phase A
10	+Ue	Electronic supply voltage
11	Mb	Motor phase B
12	Mb	Motor phase B
13	Mc	Motor phase C
14	res.	Reserved
15	Mc	Motor phase C
16	res.	Reserved